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## LARGE ENTITY TRANSMITTAL FORM

April 29, 2002

Transmitted herewith is an amendment in the above-identified application.

- The fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	23	-	27	=	0	\$18	\$0.00
INDEPENDENT	5	-	7	=	0	\$84	\$0.00
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						\$280	\$0.00
						TOTAL	\$0.00

Appl. No. 09/712,190

- ☐ Petition for ( ) month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
- ☒ No fee is required.
- ☐ Check(s) in the amount of \$0.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 

Joseph A. Kolasch, #22,463

JAK/PLS/asc  
3430-0138P

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Falls Church, VA 22040-0747  
(703) 205-8000

ATTACHMENT

(Rev. 09/27/01)



#8B Andl.  
M. Baynsn  
5/8/02  
PATENT  
3430-0138P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Woong-Kwon KIM Conf.: 8112  
Appl. No.: 09/712,190 Group: 2871  
Filed: November 15, 2000 Examiner: H. Nguyen  
For: LIQUID CRYSTAL DISPLAY DEVICE AND  
METHOD OF MANUFACTURING THE SAME

**AMENDMENT**

Assistant Commissioner for Patents  
Washington, DC 20231

Monday, April 29, 2002

Sir:

In reply to the non-final Office Action dated January 28, 2002, the following amendments and remarks are respectfully submitted under 37 C.F.R. § 1.111 in connection with the above-identified application.

**In the Claims**

Please amend the claims as follows:

1. (Amended) A liquid crystal display (LCD) device comprising:
- a substrate;
  - a gate electrode over the substrate;
  - a semiconductor layer aligned with the gate electrode;
  - an insulation layer between the gate electrode and the semiconductor layer;
  - a source electrode and a drain electrode electrically connected with the

B1  
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C1

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